CLAIMS:

1.   A data-processing system comprising

-         a microprocessor [PRC], and

-         a communication device [COM] communicating with an electronic module [MOD] intended to send a convention signal to the microprocessor, characterized in that the data-processing system comprises a hardware circuit [HARD] allowing inversion or no inversion of the order of bits of a word as a function of the value of said convention signal during transfer of said word between the electronic module [MOD] and the microprocessor [PRC].

2.   A data-processing system as claimed in claim 1, characterized in that said electronic module [MOD] is a card of the SIM type.

3.   A data-processing system as claimed in claim 1, characterized in that said hardware circuit [HARD] allows inversion or no inversion of the value of the bits of said word as a function of the value of said convention signal.

4.   A data-processing system as claimed in claim 1, characterized in that said hardware circuit [HARD] comprises switches [SWHMP] and [SWHPM], right shift registers [RXMP] and [RYPM] and left shift registers [RYMP] and [RXPM].

5.   A terminal comprising

-         a microprocessor [PRC], and

-         a communication device [COM] communicating with an electronic module [MOD] intended to send a convention signal to the microprocessor, characterized in that the terminal comprises a hardware circuit [HARD] allowing inversion or no inversion of the order of bits of a word as a function of the value of said convention signal during transfer of said word between the electronic module [MOD] and the microprocessor [PRC].

6.   A terminal as claimed in claim 5, characterized in that said electronic module [MOD] is a card of the SIM type.

7.   A terminal as claimed in claim 5, characterized in that said hardware circuit [HARD] allows inversion or no inversion of the value of the bits of said word as a function of the value of said convention signal.

8.   A terminal as caimed in claim 5, characterized in that said hardware circuit [HARD] comprises switches [SWHMP] and [SWHPM], right shift registers [RXMP] and [RYPM] and left shift registers [RYMP] and [RXPM].